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[PDF] ► A 14-bit intrinsic accuracy Q random walk CMOS DAC

GAM Van der Plas, J Vandenbussche, W ... - IEEE J. Solid-State Circuits, 1999 - iroi.seu.edu.cn

... been implemented in a single-poly, triple-metal 0.5 ... 9), far away and well shielded

from the sensitive ... Digital and analog power supplies have been separated: 2.5 ...

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Comprehensive frequency-dependent substrate noise analysis using boundary element ...-

► psu.edu [PDF]

H Li, J Carballido, HH Yu, VI Okhmatovski, ... - Proceedings of the 2002 IEEE/ACM international ..., 2002 - portal.acm.org

... guard rings. Triple-well technology can be employed for shielding of sensitive analog circuitry. Digital blocks ...

Cited by 14 - Related articles - BL Direct - All 16 versions

[PDF] ► The design of high-performance analog circuits on digital CMOS chips

EA Vittor, ... - IEEE Journal of Solid-State Circuits, 1985 - Citeseer

... for the transistor implemented in the well, in this ... This is because the device is then shielded from all ... on transistors for digital and analog applications are ...

Cited by 160 - Related articles - View as HTML - All 11 versions

Method for fabricating a shielded multilevel integrated circuit capacitor

DHS Poh, ... - US Patent 6,066,537, 2000 - Google Patents

... circuits using standard CMOS processing is well understood in ... will be coupled to the analog circuits directly ... One technique to shield the design capacitor from ...

Cited by 6 - Related articles - All 3 versions

Measuring and modeling the effects of substrate noise on the LNA for a CMOS GPS receiver

M Xu, DK Su, DK Shaeffer, TH Lee, B Wooley - IEEE Journal of Solid-State Circuits, 2001 - Springer

... was fabricated in a single-poly, triple-metal epitaxial ... to the output pad through the ground shield pad is ... the capacitance switching rates and as well as the ...

Cited by 82 - Related articles - BL Direct - All 9 versions

A practical approach to modeling silicon crosstalk in systems-on-silicon- ► csic.es [PDF]

P Van Zeijl - Workshop on Substrate Noise-coupling, IMEC, 2001 - Springer

... Use shielded bond-pads and bump-pads for all ... Use triple-well to isolate your NMOS devices from the ... grounds, and isolate the interconnect from analog parts and ...

Cited by 3 - Related articles - All 3 versions

Integrated inductive circuits

TW Wong - US Patent App. 10/134,242, 2002 - Google Patents

... By using the triple well process, a flash memory may be formed ... resistivity of the N-wells, the better the shielding of the ... the dopant used to form the N-well 44 ...

Cited by 2 - Related articles - All 6 versions

... a plurality of circuits driven by different power sources and formed on the same substrate

M Ono - US Patent 5,796,147, 1998 - Google Patents

... by As a result, even though the **triple well** construction an ... ground region 4 may serve as noises- a **shield** of the **analog** circuit 2 by means that a **well** in which ...

Cited by 1 - Related articles - All 2 versions

### System and method for preventing noise cross contamination between embedded DRAM ...

LL Hsu, RM Parent, LK Wang, MR Wordeman - US Patent 6,349,067, 2002 - Google Patents

... 27 nMOS transistor devices, said integrated **triple well** structure are ... for comprising a p-type **well** structure having ... that two buses are **shielded well** structure. ...

Cited by 4 - Related articles - All 2 versions

### Mixed Signal Design-Design Issues

HJ Casier, A Microelectronics - **Analog** circuit design: high-speed **analog-to-digital** .... 2000 - books.google.com

... for special technologies, such as BiCMOS, **triple well** CMOS or ... by a properly contacted n-**well** underneath. ... a sensitive signal line with **shielding** metal layers. ...

Related articles

### MINIMIZING UNDESIED COUPLING AND INTERACTION IN MIXED SIGNAL ICs.

TJ Schmerbeck - **Analog** Circuit Design: Fractional-N Synthesizers, ..., 2003 - Springer

... **Triple well**, mixed-signal CMOS, processes providing **well** ... low induc- tance packages, **shielding**/isolation structures ... on- chip substrate ties, n-**well** ties, power ...

Related articles - All 3 versions

### Design Aspects for Mixed Analog-Digital Circuits

F Dielacher, SE Mikroelektronik - **Analog** circuit design: mixed A/D circuit design, sensor ..., 1994 - books.google.com

... **1C** that includes **triple** 30 MHz ADC's, **triple** DSP blocks and ... pads, bonding wires, diffused guard rings, **shielding** by grounding a **well**-diffusion under ...

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### [PDF] ► A 256-Mb SDRAM using a register-controlled **digital** DLL

A Hatakeyama, H Mochizuki, T Aikawa, M ... - IEEE Journal of Solid-State Circuits, 1997 - irori.seu.edu.cn

... The cell transistors are in a **triple-well** with V back bias ... the concentration of P and N **wells** enables the ... cell structure because bit lines are not **shielded** by a ...

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### [PDF] ► A 3-V GSM baseband transmitter

CS Wong - IEEE Journal of Solid-State Circuits, 1999 - irori.seu.edu.cn

... transmitter implemented in a 0.35-  $\mu$ m **triple-metal** **digital** ... N-well resistors are available but linearity is limited ... buffer, the SCF output is **shielded** from the ...

Cited by 12 - Related articles - View as HTML - BL Direct - All 7 versions

### [PDF] ► A 1-V 10-MHz clock-rate 13-bit CMOS 16 modulator using unity-gain-reset opamps

M Keskin, U Moon, G Temes - IEEE J. Solid-State Circuits, 2002 - engr.oregonstate.edu

... of the prototype IC, realized in a 0.35-  $\mu$ m double-poly **triple-metal** CMOS ... **Well** and substrate guard strips and rings were also used to **shield** the sensitive ...

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### [BOOK] **Analog** design for CMOS VLSI systems

F Maloberti - 2001 - books.google.com

... and the augmented resolution of **digital** proc- essors ... of integrated resistors, capacitors and **analog** switches ... that the reader knows limits and performances **well**. ...

Cited by 68 - Related articles - BL Direct

### Multichannel mixed-mode IC for **digital** readout of silicon strip detectors

P Gryboś, W Dąbrowski, P Hottowy, R ... - Microelectronics Reliability, 2002 - Elsevier  
... to ensure effective functioning of such **shield rings** of ... The lines are connected via **triple bonding pads** placed ... are filled with **substrate** or **well** contacts tied ...  
Cited by 8 - Related articles - BL Direct - All 3 versions

### Interconnect Parasitic Extraction Tool for Radio-Frequency Integrated Circuits

J Lesoot, FJK Clement - Lecture notes in computer science, 2003 - Springer  
... complex 3-D structures – such as **triple wells**, deep trenches, etc ... of **substrate** taps, **ground planes** or **shielding wires** in ... expertise in RF design as **well** as for ...  
Related articles - BL Direct - All 3 versions

### [PDF] ► Wireless-Trench technology for portable wireless applications

T Johansson - Ericsson Review, 2001 - cnscenter.future.co.kr  
... The **substrate** contacts can also be used to improve **shielding** and re-duce ... products for GSM 900 and 1800/1900 MHz as **well** as GSM dual/**triple-band**, PDC ...  
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### [PDF] ► A Bluetooth radio in 0.18 m CMOS

P van Zeijl, JWT Eikenbroek, PP Vervoort, S ... - IEEE J. Solid-State Circuits, 2002 - vergina.eng.auth.gr  
... or **isola-tion** (use extra layout measures such as **shielding** or **triple-well**), or **compensation** ... it is not practical to include them all in **analog** simulations. ...  
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### Stable and efficient reduction of **substrate** model networks using congruence transforms-

► [york.ac.uk \(pdf\)](#)

KJ Kerns, IL Wemple, AT Yang - Proceedings of the 1995 IEEE/ACM International ..., 1995 - portal.acm.org  
... This algorithm utilizes the **well-conditioned** Lanczos ... Specifically, **monolithic** mixed-signal ICs are ... characterized by parasitic **analog-digital** interactions which ...

Cited by 116 - Related articles - BL Direct - All 12 versions

### [PDF] ► A **monolithic** surface micromachined accelerometer with **digital** output

C Lu, M Lemkin, BE Boser - IEEE Journal of Solid-State Circuits, 1995 - eecs.berkeley.edu

... **Shielding** is required, for example, under the suspension to prevent ... such as active suspension as **well** as a ... 25] R. van de Plassche, Integrated **Analog-to-Digital** ...

Cited by 112 - Related articles - BL Direct - All 8 versions

### Active pixel sensor having intra-pixel charge transfer with **analog-to-digital** converter

ER Fossum, SK Mendis, B Pain, RH Nixon, Z ... - US Patent 6,665,013, 2003 - Google Patents

... radiation damage, they require good light **shielding** to avoid ... other types of imager devices have problems as **well**. ... to the sensing node and an **analog-to-digital** ...

Cited by 23 - Related articles - All 4 versions

### Efficient parasitic **substrate** modeling for **monolithic** mixed-A/D circuit design and verification

KJ Kerns, IL Wemple, AT Yang - Analog integrated Circuits and Signal Processing, 1996 - Springer

... by an efficient reduction algorithm which uses a **well-conditioned** Lanczos ... requirements for both high speed **digital** and high precision **analog** circuitry pro ...

Cited by 16 - Related articles - BL Direct - All 2 versions

### Optically patterned RF **shield** for an integrated circuit chip for **analog** and/or **digital** ...

AA Immorlica Jr, RF Chase - US Patent 5,151,769, 1992 - Google Patents

... beam (both of which are disclosed), as **well** as to ... top and completing the cage-like sides of said RF **shield**. 2. In a module for **analog** and/or **digital** operation ...

Cited by 8 - Related articles - All 3 versions

### Microprocessor having environmental sensing capability

L Ristic, WC Dunn, BF Cambou, LE Terry, ... - US Patent 5,291,607, 1994 - Google Patents

... The microprocessor is **shielded** from an environmental signal ... compensate sensor output, as **well** as active ... operation, most sensors generate **analog** signals which ...

Cited by 17 - Related articles - All 2 versions

### Monolithically integrated semiconductor device containing bipolar junction transistors, ...

F Bertotti, C Cini, C Contiero, P Gaibali - US Patent 4,887,142, 1989 - Google Patents

... That is all the **analog/digital** signals handling circuit sections ... the so-called p-tubs ("p-well") which constitute ... wall, of electrical contact and **shield** of the ...

Cited by 20 - Related articles - All 2 versions

### Closed-loop planar linear motor with integral **monolithic** three-degree-of-freedom AC-...

RL Hollis Jr, ZJ Butler, AA Rizzi, AE Ouard III - US Patent 6,175,169, 2001 - Google Patents

... tooth fine control by interpolating the **analog** waveform. ... sensors are also difficult to **shield** from unwanted ... orthogonal directions of translation as **well** as a ...

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### Integrated circuit transformer with inductor-substrate isolation

GR Gutierrez - US Patent 5,969,590, 1999 - [Google Patents](#)

... OTHER PUBLICATIONS Yannis Tsvividis, "Mixed **Analog-Digital** VLSI Devices ... the second spiral inductor, while **shielding** it from ... of 1C capacitors are **well** established ...

[Cited by 5](#) - [Related articles](#) - [All 2 versions](#)

### Design Aspects for Mixed **Analog-Digital** Circuits

F Dielacher, SE Mikroelektronik - **Analog** circuit design: mixed A/D circuit design, sensor .... 1994 - [books.google.com](#)

... precautions like power supply filters, separate **analog** and **digital** ... bonding wires, diffused guard rings, **shielding** by grounding a **well**-diffusion under ...

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**Monolithic** integrated active sensors: a combination technology for thin-film and ...  
H Rudolph, K Vernis, D Seitzer - **Analog** Integrated Circuits and Signal Processing, 1993 - Springer  
... to deliver a standardized Output signal, either **analog** (eg, cur ... oxide or nitride film is a **well-known** standard ... This alloy is also a good **shielding** against the ...

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[PDF] ► A 2-V 900-MHz **monolithic** CMOS dual-loop frequency synthesizer for GSM receivers

YS Tak - 1999 - [ece.ust.hk](#)

... test structures with laminated N-well, laminated polysilicon ... is also desirable for the **analog** parts of ... achieve better compatibility to the **digital** part of the ...

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[PDF] ► A novel low-loss low-crosstalk interconnect for broad-band mixed-signal silicon MMIC's

J Kim, Y Qian, G Feng, P Ma, J Judy, MF Chang ... - IEEE Transactions on Microwave Theory and ..., 1999 - Citeseer

... serious noise crosstalk between **digital** and RF/**analog** circuits ... form the top ground plane for **shielding** and self ... line for insertion-loss measurement, as **well** as a ...

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### Parallel programmable charge domain device

TL Vogelsohn, JJ Tiemann - US Patent 4,625,293, 1986 - [Google Patents](#)

... Careful balancing and **shielding**, as **well** as on ... to be quite practical if careful **shielding** and proper ... device for multiplying a sampled **analog** signal represented ...

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[PDF] ► Radio frequency integrated circuits for portable communications

AA Abidi - Proceedings of the 1994 IEEE Custom Integrated ..., 1994 - [geocities.com](#)

... insulating **substrate** and gold metallization were **well-suited** to ... levels and with the usual RF **shielding** in the ... ICs (Fig.7) will freely mix **analog** and **digital** ...

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[BOOK] **Analog** design for CMOS VLSI systems

F Maloberti - 2001 - [books.google.com](#)

... and the augmented resolution of **digital** proc- essors ... of integrated resistors, capacitors and **analog** switches ... that the reader knows limits and performances **well**. ...

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[PDF] ► Impact of On-Chip Interconnect Frequency-Dependent on **Digital** and RF Design

VI Conclusion - ... Trans. Circuits Syst. II, **Analog Digit. Signal Process**, 2000 - visida.eecs.umich.edu  
... **for well-shielded structures (eg, clocks), ignoring multiple ... accurate analysis and effective shielding of noise. ... supply noise in sensitive analog circuit blocks ...**  
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[PDF] ► **A direct digital synthesizer with an on-chip D/A-converter**  
J Vankka, M Waltari, M Kosunen, KAI Halonen - IEEE Journal of Solid-State Circuits, 1998 - iro1.seu.edu.cn  
... **A converter is based on a well-known weighted ... Horizontal and vertical shielding between the analog signals ... A monolithic 10-b digital-to-analog converter using ...**  
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**Monolithic magnetic read-while-write head apparatus and method of manufacture**  
RG Biskeborn, JH Eaton - US Patent App. 09/877,382, 2001 - Google Patents  
... **However, additional conductive shield- ing layers may be added ... As is well-known in the tape drive ... QIC), digital linear tape (DLT), digital analog tape (DAT ...**  
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[PDF] ► **Modeling of metal and substrate losses in CMOS and BiCMOS inductors for RFICs**  
P Arcioni, R Castello, L Perreggini, E Sacchi, F ... - Microwave Journal, 1999 - spazioinwind.libero.it  
... **in replacing bipolar and GaAs in the RF analog part ... with the intro- duction of a polysilicon shield below the ... ind2B, that Q and Q' com- pare quite well at every ...**  
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AA Immorlica Jr, RF Chase - US Patent 5,151,769, 1992 - Google Patents

... **digital** mode and deals with the **shielding** of that ... A common module for mixed **analog/digital** operation ... for phased array systems for **satellite** communication or ...

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### Silicon Satellites

F JANSON - Second Annual Symposium on Frontiers of ..., 1997 - books.google.com

... most of the mass of a **satellite**; it can ... heat transfer system, radiation **shield**, optic, and ... low-power **digital** circuits, low-power **analog** circuits, silicon ...

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### [PDF] ► Development of high-performance monolithic CMOS detectors for space applications

F Larnaudie, B Vignon, O Saint-Pe, M Tuleu, ... - Photonics for space environments VIII: 9-10 July, 2002 ..., 2002 - oatao.univ-toulouse.fr

... driving electronics within the **satellite** platform avionics ... and therefore (>10 mm equivalent **shielding**) with Galileo ... the treatment of the **analog** output signal ...

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### Chip-antenna apparatus and method

CC Tsai ... - US Patent App. 10/044,356, 2002 - Google Patents

... frequency identification (RFID), various **satellite**-based com ... respect to the **digital** and **analog** circuitry has ... **Shielding** techniques have generally proven to be ...

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### Analog integrated filters or continuous-time filters for LSI and VLSI- ► journaldephysique.org

[PDF]

JO Voorman - Revue de Physique Appliquée, 1987 - edpsciences.org

... 1. - Mixed **analog/digital** interfaces between an **analog** outside world and a **digital** processor. ... 2. - **Analog** filters, sampled-signal filters and **digital** filters. ...

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### RF power device with on-chip digital control and optical interface

RS Bickham, J Schultz, D Anderson, M Van ... - US Patent App. 10/680,836, 2003 - Google Patents

... as silver-silicone elastomer, to provide electromagnetic **shielding**. ... from a **satellite** via **satellite** antenna 714 ... applications like GSM and **analog** power amplifiers ...

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### [PDF] ► A Single-Chip 900-MHz Spread-Spectrum Wireless Transceiver in 1- m CMOS Part II:

...

A Rofougaran, G Chang, JJ Rael, JYC Chang, ... - IEEE Journal of Solid State Circuits, 1998 - Citeseer

... A **digital** correlating detector makes the data decisions. ... The circuit includes **digital** phase-locked loops to track symbol timing and frequency hopping. ...

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[PDF] ► [HBT Technology and Applications, Today and Tomorrow](#)

F Lee, G Kinoshita, MF Chang - 1994 - [amsacta.cib.unibo.it](#)

... but because the base is well **shielded** and sandwiched ... now possible at - 6 GHz, **satellite** communication systems ... communication systems and mixed **analog-digital** systems ...

All 2 versions

[PDF] ► [BMDO/AFRL Partnership: Advances In Data Handling Systems For Space Experiment](#)

...

J Lyke, P Brezna, K Avery - 2001 MAPLD International Conference; Laurel, MD; ..., 2001 - [klds.org](#)

... Page 5. which provides electrostatic **shielding** and emissivity control for the top surface of the **satellite**. ... 16 CHANNEL **ANALOG TO DIGITAL SELECT** ...

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[PDF] ► [CMOS active pixel image sensors for highly integrated imaging systems](#)

SK Mendis, SE Kemery, RC Gee, B Pain, CO ... - IEEE Journal of Solid-State Circuits, 1997 - [Citeseer](#)

... circuitry outside the pixel array is covered by a light shield fabricated using ... image data was captured using a 100-kHz 16-b **analog-to-digital** converter (ADC) ...

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[PDF] ► [Compactness and Versatility in a New Plug-Together Digital Multimeter](#)

A Gookin - Hewlett-Packard Journal, 1972 - [hpl.hp.com](#)

... this philosophy, the display section's **analog-to-digital** ... go directly into guard (metal **shield** that electrically ... a dc voltage into **digital** information with ...

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[PDF] ► [Integrated optoelectronic circuits with InP-based HBTs](#)

D Yap, YK Brown, RH Walden, TPE Broekaert, ... - Proceedings of SPIE, 2001 - [optics.arizona.edu](#)

... have been developed for both **digital** and **analog** ... by enclosing each channel in a metal **shield**. Another important consideration for **analog** receivers is linearity ...

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[PDF] ► [Differentially driven symmetric microstrip inductors](#)

M Danesh, JR Long - IEEE Transactions on Microwave Theory and ..., 2002 - [minadanesh.com](#)

... wireless local area networks, **satellite** communications ... and the potential for combining **analog** and **digital** ... A patterned ground **shield** has been demonstrated to ...

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MR Shortis, HA Beyer - Close range photogrammetry and machine vision, 1996 - [geomsoft.com](#)

... best known for their applications in **satellite** and airborne ... proposed to store and shift **analogue** signals. ... wavelength photons can penetrate the **shielding** of the ...

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[PDF] ► [A 1.57-GHz RF front-end for triple conversion GPS receiver](#)

F Piazza, Q Huang - IEEE Journal of Solid-State Circuits, 1998 - [vncet.com.br](#)

... Each **satellite** sends the universal time (UTC) and ... sensitive lines and partially **shielded** with metal 1 ... is in **analog** and mixed **analog-digital** integrated circuits ...

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GuG Kang - 2003 - [ohiolink.edu](#)

... Micromachined 3D Multilayer **Monolithic** Microwave Circuits", NASA Ideas ... 47 Figure 3.4 SEM micrograph of microstrip T-pattern fabricated with polyimide **substrate** ...



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### Advances in Data Handling Systems for Space Experiment Control

JC Lyke, PA Brezna, K Avery - Journal OF Spacecraft AND Rockets, 2002 - pdf.aiaa.org

... control for the top surface of the **satellite**. ... With proper **shielding**, the expected total ionizing dose exposure ... with common requirements for **analog** and **digital** ...

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### Millimeter wave multilayer assembly

WR DeMore, RA Holloway, BA Holmes, BT ... - US Patent 6,249,439, 2001 - Google Patents

... which includes integrated RF, **digital**, **analog** and antenna ... of the RF layer and provides electromag- netic **shielding**. ... view of a **satellite** communications system ...

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### [PDF] ► Microwave and millimeter-wave integrated circuits

EC Niehenke, RA Pucel, IJ Bahl - IEEE Transactions on Microwave Theory and ..., 2002 - el.vgtu.it

... matching networks, bias lines, and **shielding** of RF ... of-sight communication links, **satellite** communications, wireless ... at both frequency bands in **analog**, as well ...

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### An RF Power Amplifier in a Digital CMOS Process

P Asbeck, C Falliesen - Analog Integrated Circuits and Signal Processing, 2002 - Springer

... metal layer 6 with a poly **shield** to reduce ... **substrate** (10–20 -cm) and **analog** options ... in Denmark working on integration of **satellite** communication transceivers ...

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